



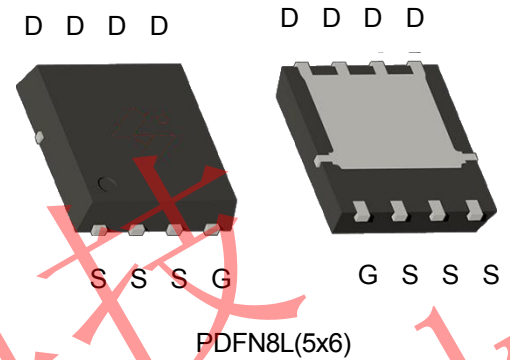
HYG025N04NA1C2

Single N-Channel Enhancement Mode MOSFET

Feature

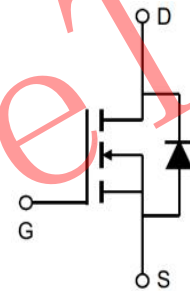
- 40V/190A
 $R_{DS(ON)} = 1.4m\Omega(\text{typ.}) @ V_{GS} = 10V$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen- Free Devices Available

Pin Description



Applications

- Load Switch
- Lithium battery protect board



Single N-Channel MOSFET

Ordering and Marking Information

<p>C2 G025N04 XYMXXXXXX</p>	<p>Package Code C2: PDFN8L(5x6)</p> <p>Date Code XYMXXXXXX</p>
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Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termination finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this product and/or to this document at any time without notice.



Absolute Maximum Ratings

HYG025N04NA1C2

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		40	V
V _{GSS}	Gate-Source Voltage		±20	V
T _J	Junction Temperature Range		-55 to 175	°C
T _{STG}	Storage Temperature Range		-55 to 175	°C
I _S	Source Current-Continuous(Body Diode)	Tc=25°C	190	A
Mounted on Large Heat Sink				
I _{DM}	Pulsed Drain Current *	Tc=25°C	720	A
I _D	Continuous Drain Current	Tc=25°C	190	A
		Tc=100°C	135	A
P _D	Maximum Power Dissipation	Tc=25°C	130	W
		Tc=100°C	65	W
R _{θJC}	Thermal Resistance, Junction-to-Case		1.15	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient **		45	°C/W
E _{AS}	SinglePulsed-Avalanche Energy ***	L=0.1mH	393	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.

** Surface mounted on FR-4 board.

*** Limited by T_{Jmax}, starting T_J=25°C, L = 0.1mH, R_G=25Ω, V_{GS}=10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG025N04NA1			Unit
			Min	Typ.	Max	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	40	-	-	V
I _{DSS}	Drain-to-Source Leakage Current	V _{DS} =40V, V _{GS} =0V	-	-	1	μA
		T _J =125°C	-	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2	3	4	V
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _{DS} =60A	-	1.4	1.8	mΩ
Diode Characteristics						
V _{SD} *	Diode Forward Voltage	I _{SD} =60A, V _{GS} =0V	-	0.8	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =20A, dI _{SD} /dt=100A/μs	-	25.1	-	ns
Q _{rr}	Reverse Recovery Charge		-	17.6	-	nC



Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted) **HYG025N04NA1C2**

Symbol	Parameter	Test Conditions	HYG025N04NA1			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	4.5	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, Frequency=1.0MHz	-	5744	-	pF
C _{oss}	Output Capacitance					
C _{rss}	Reverse Transfer Capacitance					
t _{d(ON)}	Turn-on Delay Time	V _{DD} =20V, R _G =4Ω, I _{DS} =60A, V _{GS} =10V	-	16.7	-	ns
T _r	Turn-on Rise Time					
t _{d(OFF)}	Turn-off Delay Time					
T _f	Turn-off Fall Time					
Gate Charge Characteristics						
Q _g	Total Gate Charge (V _{GS} =10V)	V _{DS} =32V, V _{GS} =10V, I _{DS} =60A	-	122.2	-	nC
Q _{gs}	Gate-Source Charge					
Q _{gd}	Gate-Drain Charge					

Note: *Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%



Typical Operating Characteristics

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Figure 1: Power Dissipation

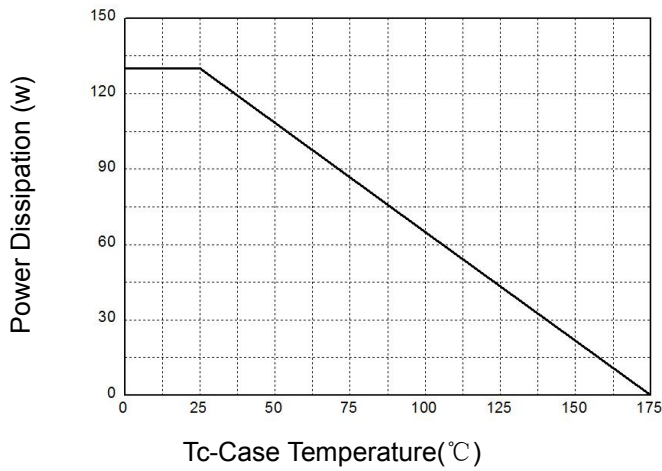


Figure 2: Drain Current

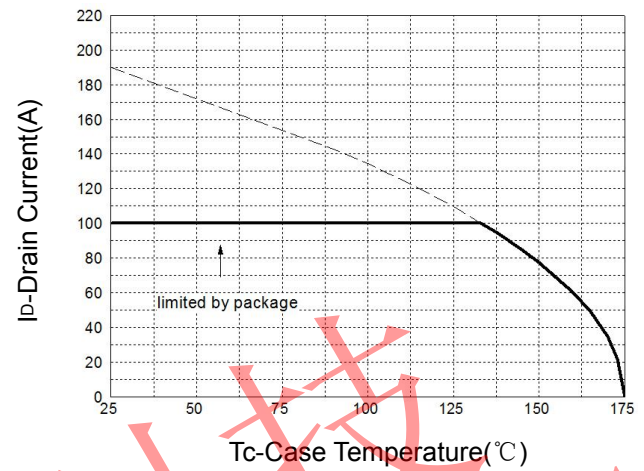


Figure 3: Safe Operation Area

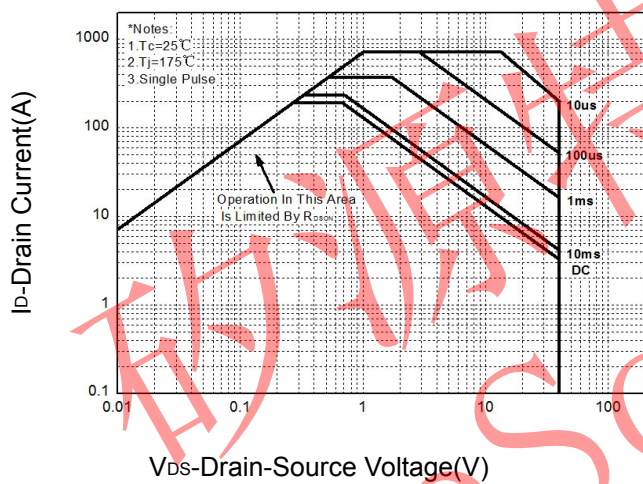


Figure 4: Thermal Transient Impedance

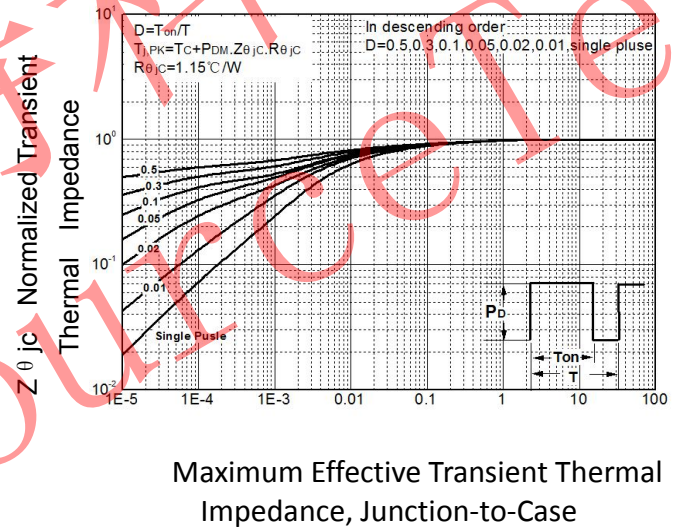


Figure 5: Output Characteristics

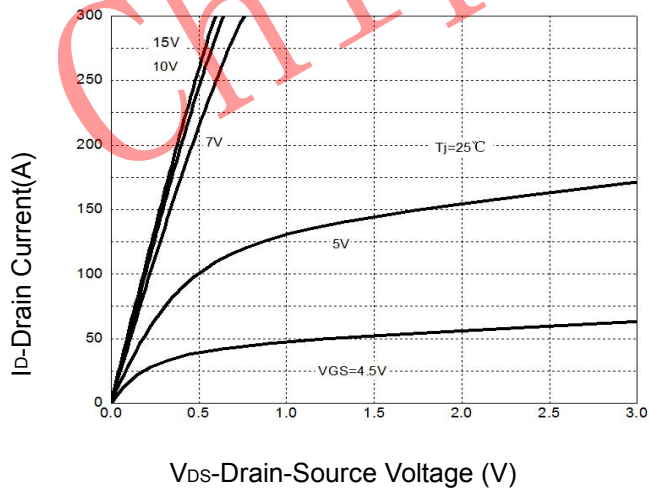
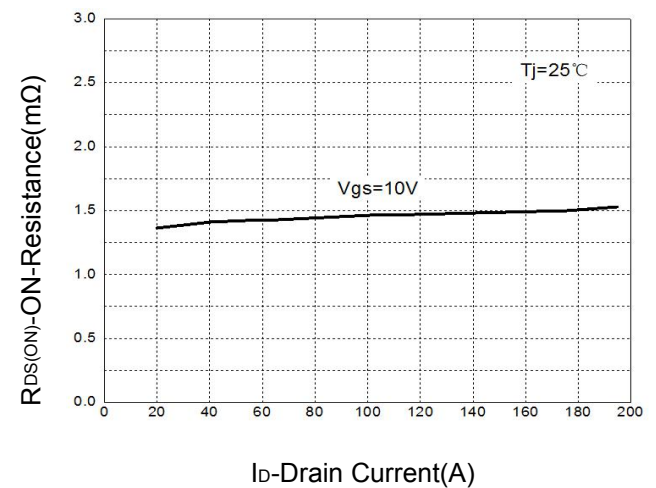


Figure 6: Drain-Source On Resistance





Typical Operating Characteristics(Cont.)

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Figure 7: On-Resistance vs. Temperature

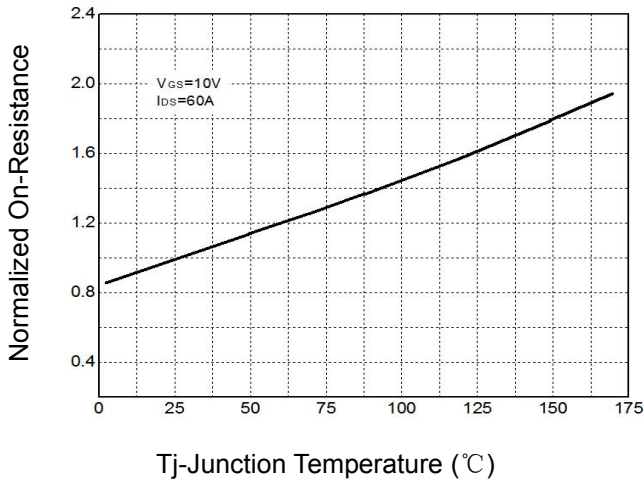


Figure 8: Source-Drain Diode Forward

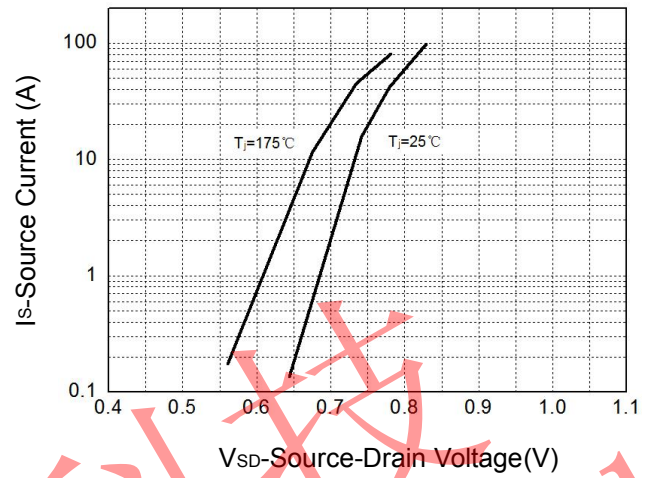


Figure 9: Capacitance Characteristics

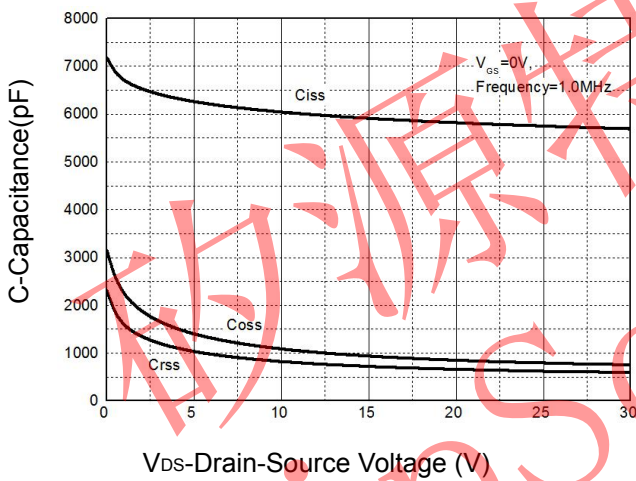
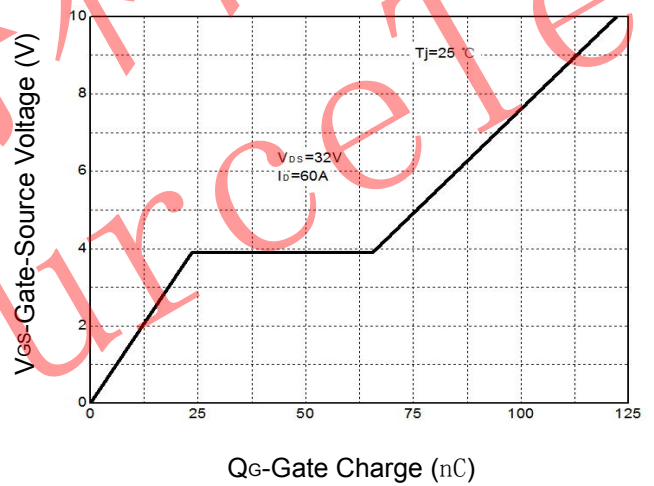


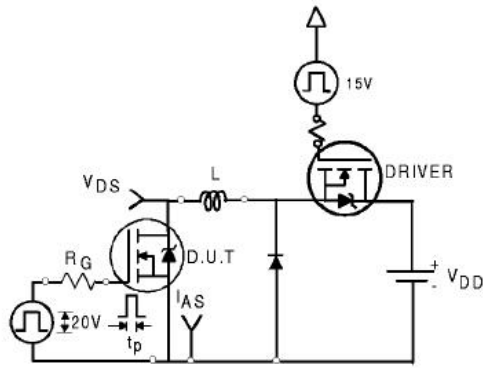
Figure 10: Gate Charge Characteristics



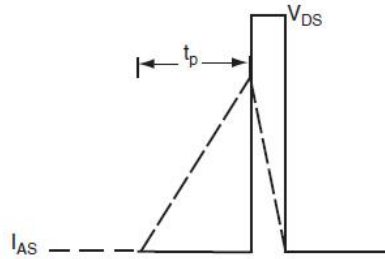


Avalanche Test Circuit

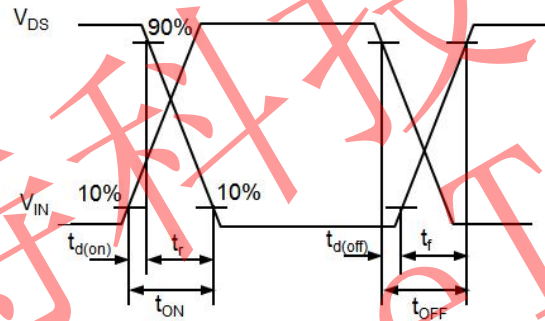
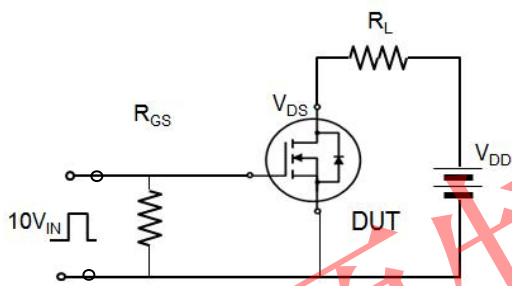
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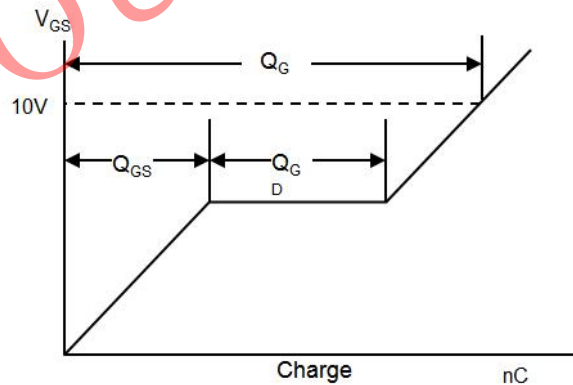
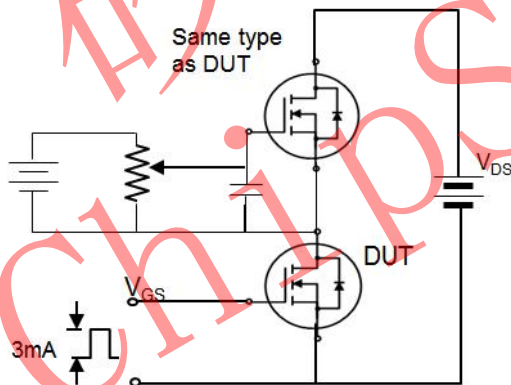
$$E_{AS} = \frac{1}{2} L I_{AS}^2$$



Switching Time Test Circuit



Gate Charge Test Circuit





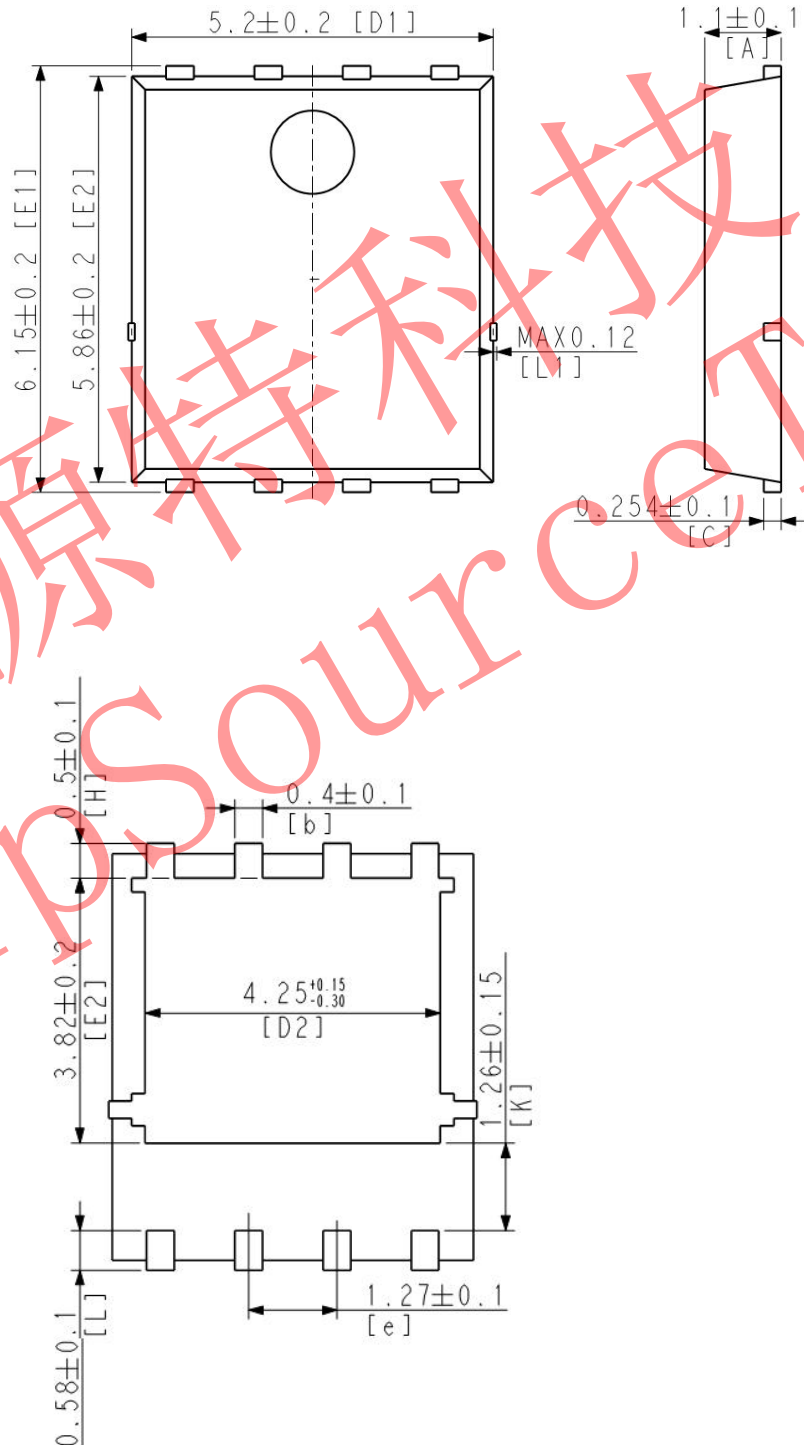
Device Per Unit

HYG025N04NA1C2

Package Type	Unit	Quantity
PDFN8L(5x6)	Reel	5000

Package Information

PDFN8L(5x6)





HYG025N04NA1C2

*Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.

** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³	Volume mm ³
	<350	≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³	Volume mm ³	Volume mm ³
	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168 Hrs /500 Hrs /1000 Hrs, Bias @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C